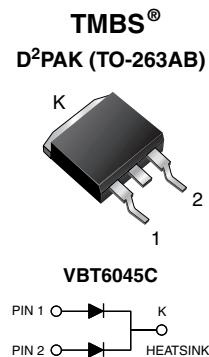


## Dual Low-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low  $V_F = 0.33$  V at  $I_F = 10$  A



### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	2 x 30 A
$V_{RRM}$	45 V
$I_{FSM}$	320 A
$V_F$ at $I_F = 30$ A	0.47 V
$T_J$ max.	150 °C
Package	D2PAK (TO-263AB)
Circuit configuration	Common cathode

### FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT

### TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

### MECHANICAL DATA

**Case:** D2PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating  
Base P/N-E3 - RoHS-compliant, commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

**Polarity:** as marked

**Mounting Torque:** 10 in-lbs maximum

MAXIMUM RATINGS ( $T_A = 25$ °C unless otherwise noted)			
PARAMETER	SYMBOL	VBT6045C	UNIT
Maximum repetitive peak reverse voltage	$V_{RRM}$	45	V
Maximum average forward rectified current (fig. 1)	$I_{F(AV)}$	60	A
per device per diode		30	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	$I_{FSM}$	320	A
Operating junction and storage temperature range	$T_J, T_{STG}$	-40 to +150	°C

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25^\circ\text{C}$ unless otherwise noted)					
PARAMETER	TEST CONDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	$I_F = 10 \text{ A}$	$V_F$ <sup>(1)</sup>	0.44	-	V
	$I_F = 15 \text{ A}$		0.47	-	
	$I_F = 30 \text{ A}$		0.54	0.64	
	$I_F = 10 \text{ A}$	$V_F$ <sup>(1)</sup>	0.33	-	
	$I_F = 15 \text{ A}$		0.37	-	
	$I_F = 30 \text{ A}$		0.47	0.56	
Reverse current per diode	$V_R = 45 \text{ V}$	$I_R$ <sup>(2)</sup>	-	3000	$\mu\text{A}$
			18	50	mA

**Notes**

(1) Pulse test: 300  $\mu\text{s}$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width  $\leq 40 \text{ ms}$

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25^\circ\text{C}$ unless otherwise noted)					
PARAMETER	SYMBOL	VBT60405C			UNIT
Typical thermal resistance	per diode	$R_{\theta\text{JC}}$	1.5	0.8	$^\circ\text{C/W}$
	per device		0.8		

<b>ORDERING INFORMATION</b> (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
D <sup>2</sup> PAK (TO-263AB)	VBT6045C-E3/4W	1.38	4W	50/tube	Tube
D <sup>2</sup> PAK (TO-263AB)	VBT6045C-E3/8W	1.38	8W	800/reel	Tape and reel

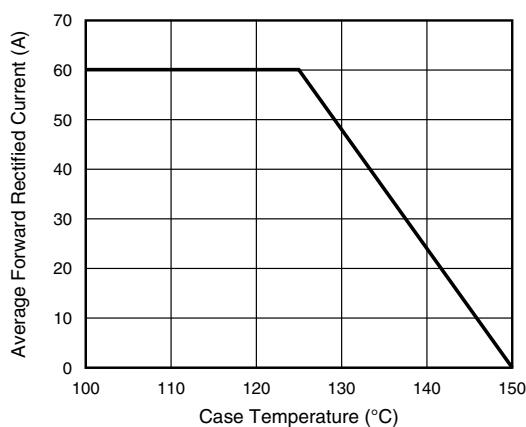
**RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)


Fig. 1 - Maximum Forward Current Derating Curve

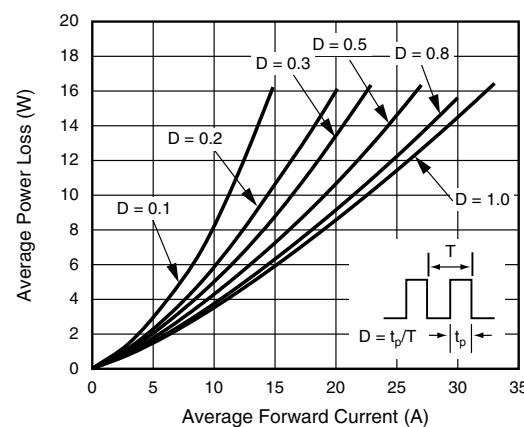


Fig. 2 - Forward Power Loss Characteristics Per Diode

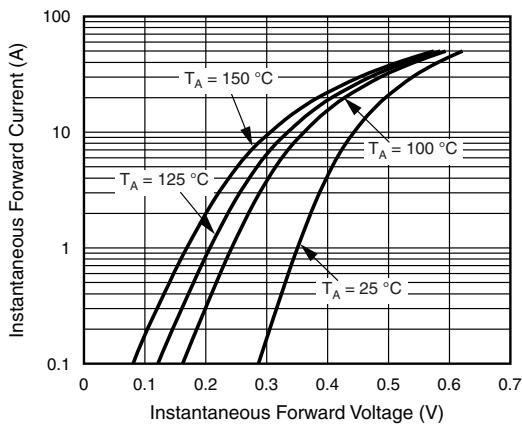


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

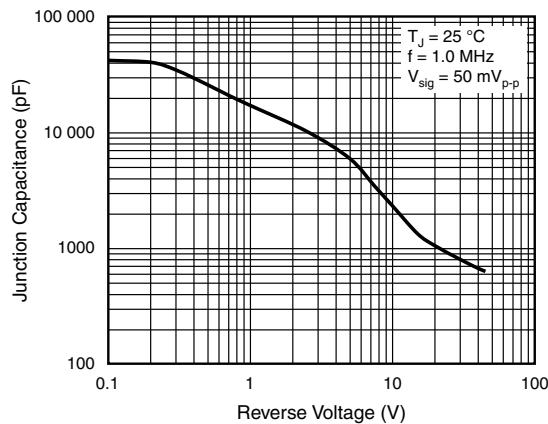


Fig. 5 - Typical Junction Capacitance Per Diode

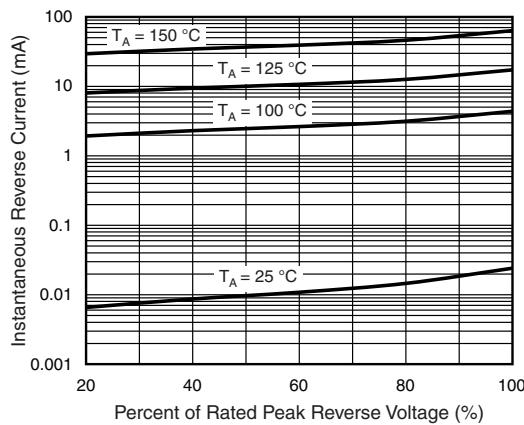


Fig. 4 - Typical Reverse Characteristics Per Diode

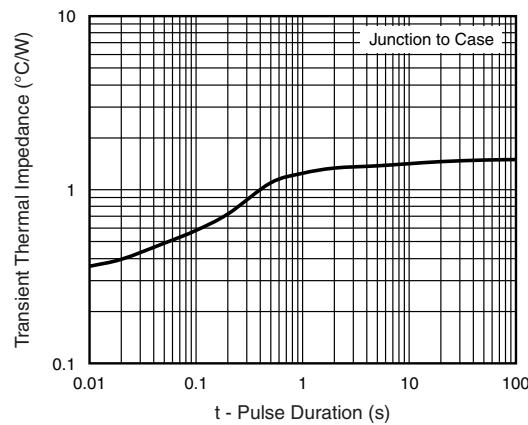
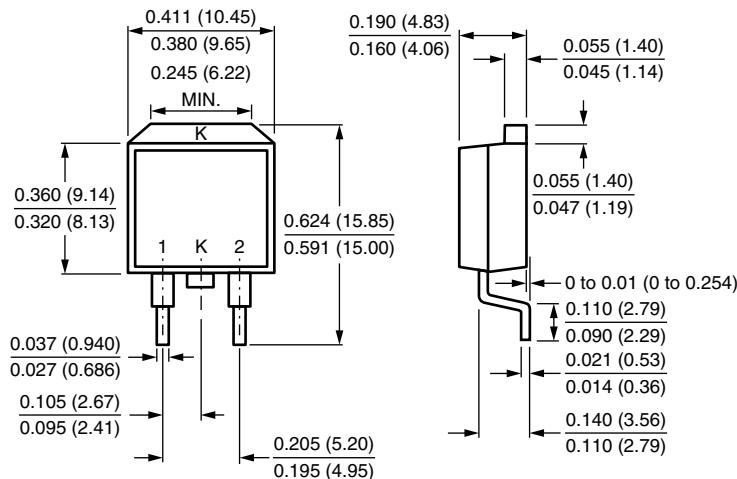


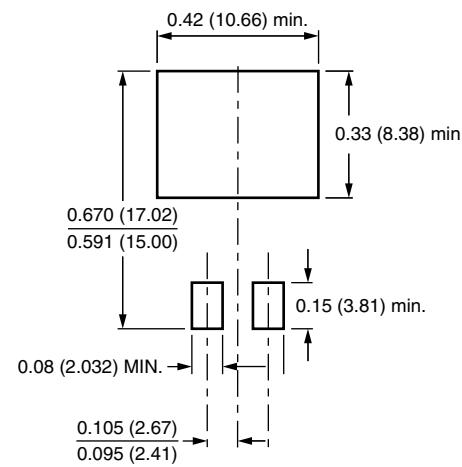
Fig. 6 - Typical Transient Thermal Impedance Per Diode

**PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

## D<sup>2</sup>PAK (TO-263AB)



## Mounting Pad Layout



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