

Product Summary

V_R (V)	I_F (A)	V_F MAX (V) @ +25°C	I_R MAX (mA) @ +25°C
60	1.0	0.50	0.1

Description and Applications

These Schottky barrier rectifiers have been designed to meet the stringent requirements of automotive applications. They are ideally suited to use as:

- Polarity protection diodes
- Re-circulating diodes
- Switching diodes

Features and Benefits

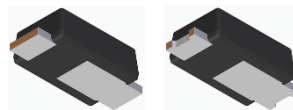
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DFLS160Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: PowerDI[®]123
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202 Method 208 ^(e3)
- Weight: 0.01 grams (Approximate)

PowerDI123



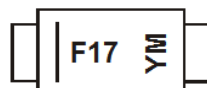
Bottom View

Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
DFLS160Q-7	PowerDI123	3000	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



F17 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: M = 2025)
 M = Month (ex: 9 = September)

Date Code Key

Year	2014	-	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	B	-	M	N	P	R	S	T	U	V	W	X

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	60	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R		
RMS Reverse Voltage	V _{R(RMS)}	42	V
Average Forward Current	I _{F(AV)}	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	I _{FSM}	50	A
Electrostatic Discharge	HBM	4000	V
Electrostatic Discharge	MM	400	V
Electrostatic Discharge	CDM	1	kV

Thermal Characteristics

Characteristic	Symbol	Typ	Max	Unit
Thermal Resistance Junction to Soldering Point (Cathode) (Note 5)	R _{θJS}	—	6	°C/W
Thermal Resistance Junction to Ambient (Note 6)	R _{θJA}	125	—	°C/W
Thermal Resistance Junction to Ambient (Note 7)	R _{θJA}	60	—	°C/W
Typical Thermal Resistance to Case (Note 8)	R _{θJC}	—	18	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150		°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 9)	V _{(BR)R}	60	—	—	V	I _R = 0.2mA
Forward Voltage	V _F	—	—	0.50	V	I _F = 1.0A
Leakage Current (Note 9)	I _R	—	—	0.1	mA	V _R = 60V, T _A = +25°C
Total Capacitance	C _T	—	67	—	pF	V _R = 10V, f = 1.0MHz
Switching Speed	t _{RR}	—	12	—	ns	I _F = 0.5A, I _R = 1A, I _{RR} = 0.25A (RG1)

- Notes:
- Theoretical R_{θJS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
 - Device mounted on Polyimide substrate, 1" x 1" 2oz copper double-sided PC board with minimum recommended pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
 - Part mounted on 50.8mm*50.8mm GETEK board with 25.4mm*25.4mm copper pad, 25% anode, 75% cathode. T_A = +25°C
 - Part mounted on FR-4 board with 1.8mm X 2.5mm cathode and 1.8mm X 1.2mm anode, 1 oz. copper pads. T_A = +25°C
 - Short duration pulse test to minimize self-heating effect

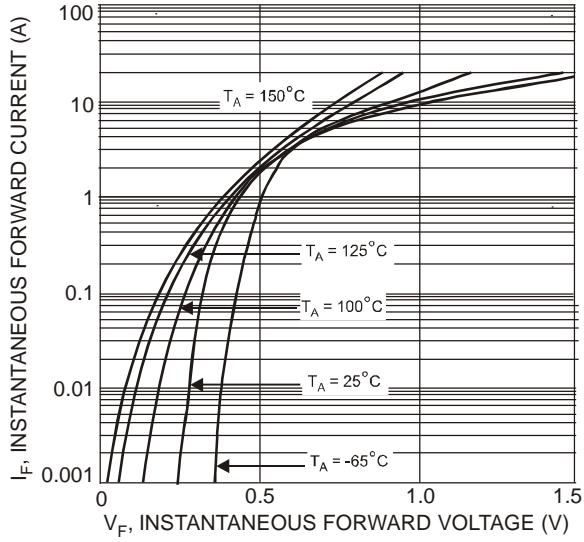


Fig. 1 Typical Forward Characteristics

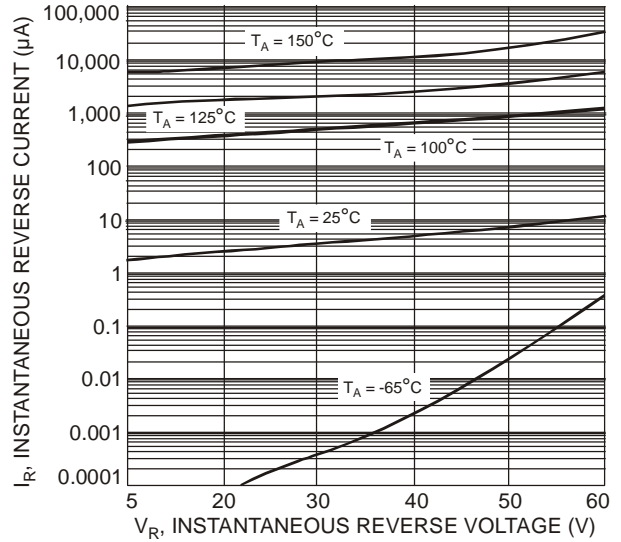


Fig. 2 Typical Reverse Characteristics

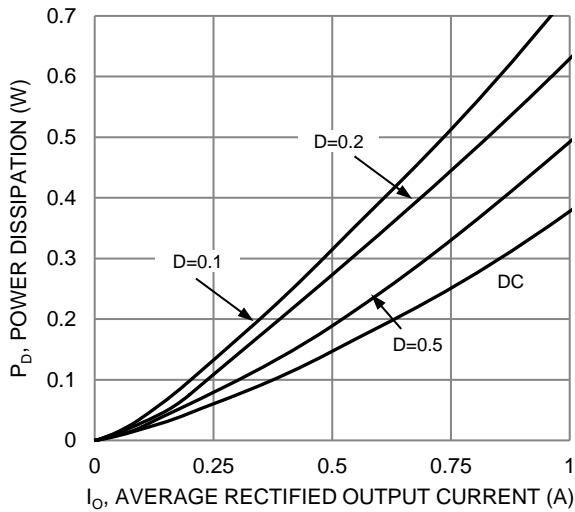


Fig. 3 Forward Power Dissipation $T_J=125^\circ\text{C}$

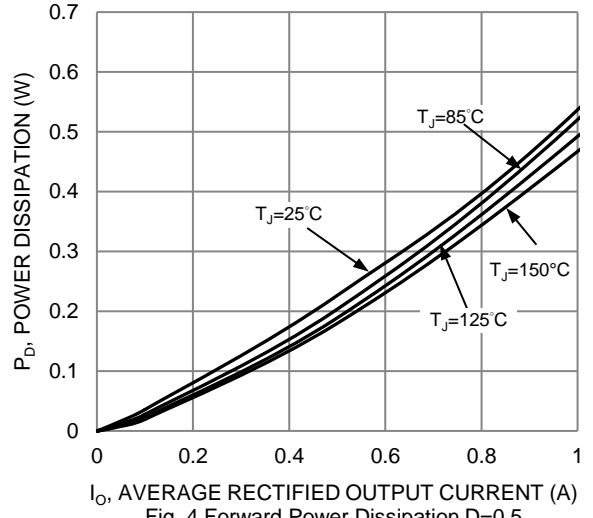


Fig. 4 Forward Power Dissipation $D=0.5$

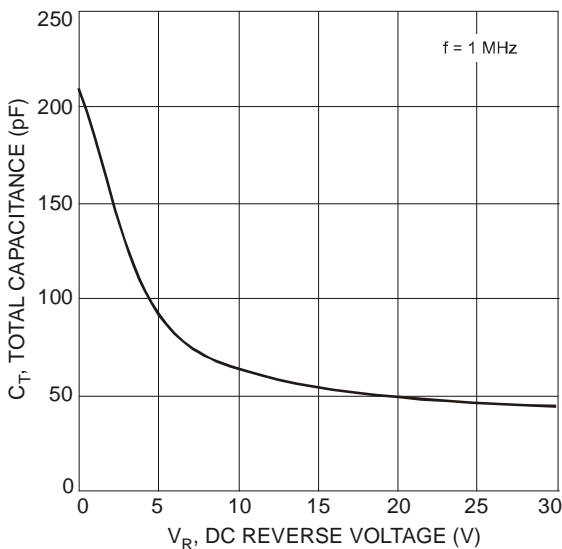


Fig. 5 Total Capacitance vs. Reverse Voltage

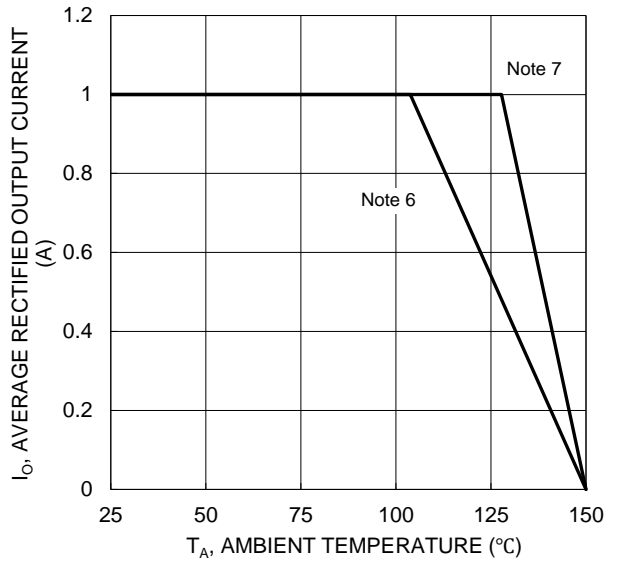
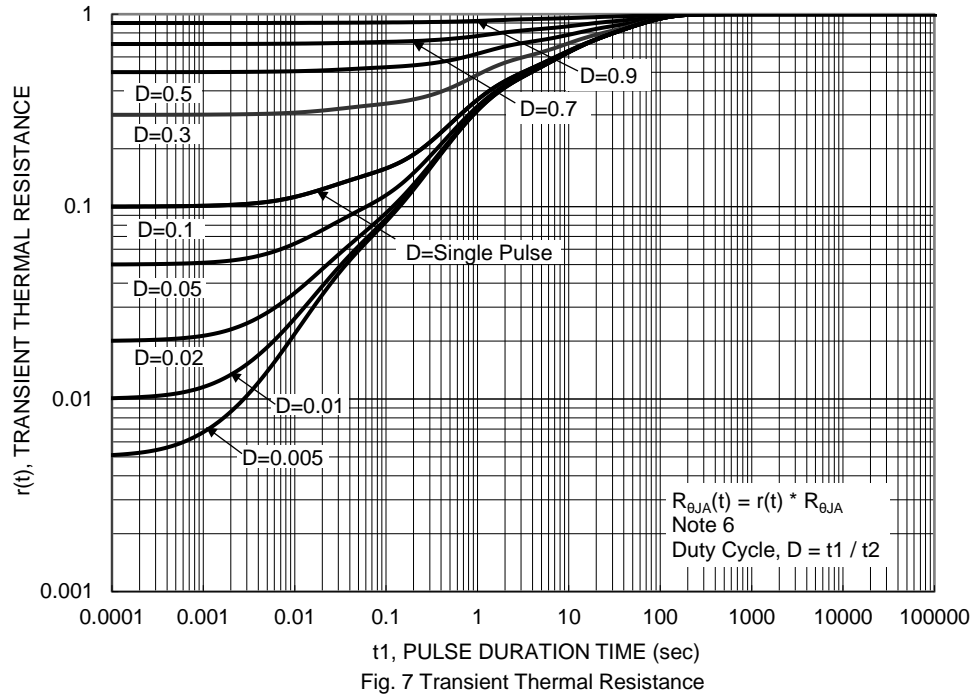


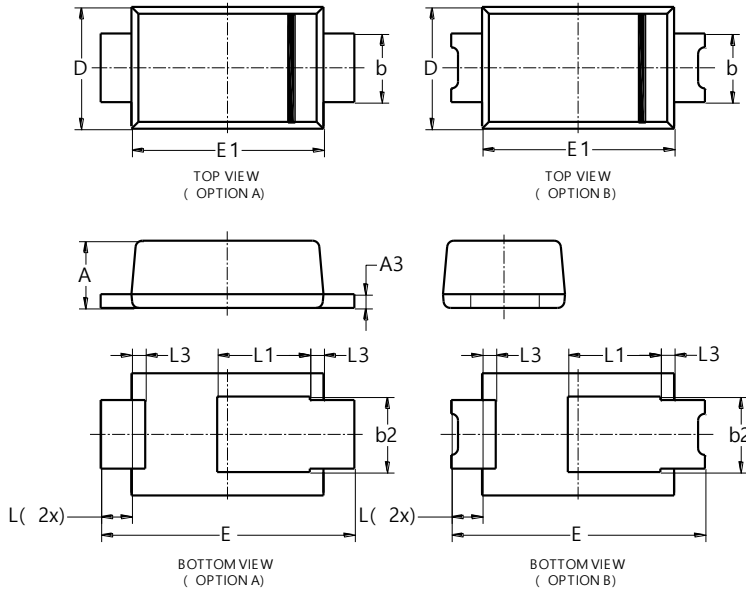
Fig. 6 DC Forward Current Derating



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI123

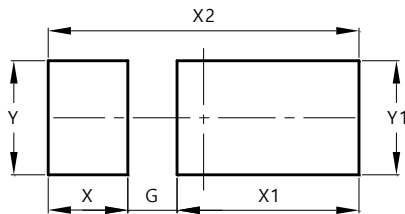


PowerDI123			
Dim	Min	Max	Typ
A	0.93	1.00	0.98
A3	0.15	0.25	0.20
b	0.85	1.25	1.00
b2	1.025	1.125	1.10
D	1.63	1.93	1.78
E	3.50	3.90	3.70
E1	2.60	3.00	2.80
L	0.40	0.50	0.45
L1	1.25	1.40	1.35
L3	0.125	0.275	0.20
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI123



Dimensions	Value (in mm)
G	0.65
X	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50

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